



	Class	Subclass
ISSUE CLASSIFICATION		

PATENT NUMBER

U.S. UTILITY Patent Application

<p>O.I.P.E.</p> <p>SCANNED <i>ACT</i> Q.A. <i>CH</i></p>	<p>PATENT DATE</p>
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CLASS	PLICATION NO. 09/591621	CONT/PRIOR	CLASS 703	SUBCLASS 14	ART UNIT 2763 2123	EXAMINER Rosalet-H CHOT
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Vidyabhusan Gupta

System and method for designing and optimizing the memory of an embedded processing system

PTO-2040
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